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[SECTION V.6: WARSHALL'S ALGORITHM TO FIND TRANSITIVE CLOSURE](#)

Definition V.6.1: Let S be the finite set $\{v_1, \dots, v_n\}$, R a relation on S . The adjacency matrix A of R is an $n \times n$ Boolean (zero-one) matrix defined by $A_{ij} = 1$ if the digraph D has an edge from v_i to v_j , $A_{ij} = 0$ if the digraph D has no edge from v_i to v_j (This is a special case of the adjacency matrix M of a directed graph in Epp p. 642. Herdefinition allows for more than one edge between two...

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of Low-Dimensional Posets Piotr Berman¹, Arnab Bhattacharyya², Elena Grigorescu³, Sofya Raskhodnikova¹, David P. Woodru⁴, and Grigory Yaroslavlsev¹ ¹ Pennsylvania State University, USA. {berman, sofya, grigory}@cse.psu.edu. ² Massachusetts Institute of Technology, USA. abhatt@mit.edu. ³ Georgia Institute of Technology, USA. elena@cc.gatech.edu. ⁴ IBM Almaden Research Center, USA. dpwoodru@u...

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[A TRANSITIVE CLOSURE BASED ALGORITHM FOR TEST GENERATION - DESIGN AUTOMATION CONFERENCE, 1991. 28TH ACM/IEEE](#)

A TRANSITIVE CLOSURE BASED ALGORITHM FOR TEST GENERATION Srimat T Chakradhar . NEC Research Institute, Princeton, NJ 08540 Vishwani D. Agrawal AT&T Bell Laboratories, Murray Hill, NJ 07974 ABSTRACT -We present a transitive closure (TC) based test In spite of making extensive use of the circuit structure and generation algorithm. A test is obtained by determining signal function, most algorithms have...

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[MODEL CHECKING AND TRANSITIVE-CLOSURE LOGIC?](#)

Neil Immerman¹ and Moshe Y. Vardi² ¹ Computer Science Dept., University of Massachusetts, Amherst, MA 01003, http: www.cs.umass.edu immerman, immerman@cs.umass.edu ² Computer Science Dept., Rice University, Houston, TX 77005-1892, http: www.cs.rice.edu vardi, vardi@cs.rice.edu Abstract. We give a linear-time algorithm to translate any formula from computation tree logic (CTL or CTL*) into ...

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